



Job Title: FPGA RTL Design Developer

Location: Hyderabad

Position Overview:

We are seeking a highly skilled and motivated FPGA RTL Design Developer to join our team. The ideal candidate will be responsible for developing and implementing RTL designs for Xilinx, Altera and Actel FPGAs, focusing on high-speed data interface and memory controllers, Communication and DSP algorithms, and efficient architecture implementations.

Responsibilities:

- Design, implement, and verify RTL designs targeting Xilinx, Altera and Actel FPGAs.
- Collaborate with cross-functional teams to understand and refine Communication and DSP algorithms for FPGA implementation.
- Create and optimize architectures for efficient resource utilization and performance.
- Perform simulation, synthesis, and timing analysis to ensure design functionality and meet performance goals.
- Debug and troubleshoot FPGA designs, identifying and resolving issues.
- Stay updated with the latest FPGA technologies and tools, incorporating best practices into design methodologies.
- Document designs, specifications, and test plans comprehensively.



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Requirements:

- Bachelor's/Master's degree in Electrical Engineering, Computer Engineering, or related field.
- Preferable experience of 1 to 2 years in RTL design and development for FPGAs, particularly with Xilinx and Actel devices.
- Strong understanding of DSP algorithms and their implementation in FPGA architectures.
- Proficiency in HDL languages (Verilog/SystemVerilog) and FPGA design tools (Vivado, Quartus).
- Familiarity with simulation tools (ModelSim, Questa) and scripting languages (Python, Tcl) for automation.
- Solid grasp of synthesis, timing closure, and FPGA optimization techniques.
- Excellent problem-solving skills and attention to detail.
- Ability to work independently and collaboratively in a team environment.
- Strong communication skills to effectively convey technical concepts and collaborate with multidisciplinary teams.

Preferred Qualifications:

- Experience with high-speed interfaces (PCIe, DDR) and SoC FPGA architectures.
- Knowledge of embedded processors (MicroBlaze, ARM) and their integration into FPGA designs.
- Familiarity with DSP hardware design, signal processing, and algorithm development.



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Benefits:

- Competitive salary package.
- Opportunities for professional growth and career advancement.
- Collaborative and innovative work environment.

How to Apply:

Please email your resume to hrm@kalki-tech.com



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